

Electronic Acknowledgement Receipt

EFS ID:	2466132
Application Number:	10825343
International Application Number:	
Confirmation Number:	8658
Title of Invention:	Minimizing transistor variations due to shallow trench isolation stress
First Named Inventor/Applicant Name:	Jong Shik Yoon
Customer Number:	23494
Filer:	Jacqueline J. Garner.
Filer Authorized By:	
Attorney Docket Number:	TI 37043
Receipt Date:	14-NOV-2007
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 1860
RAM confirmation Number	7266
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		T37043_116_OA_5142007.pdf	36646 11932303dc00a3f6c8cc24733283f74a abab8238	yes	7
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Amendment After Final			1	1	
Claims			2	3	
Applicant Arguments/Remarks Made in an Amendment			4	7	
Warnings:					
Information:					
2	Request for Continued Examination (RCE)	t37043_rce.pdf	663043 2065570c357d20a037c9025e1a29a06 03659550be	no	3
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8311 54d83b115db69f6120517c639a6b53e 57862717	no	2
Warnings:					
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Total Files Size (in bytes):			708000		

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